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A textbook for third and fourth year students in all electrical and computer engineering departments taking electronic circuit courses. . Every chapter features a design problem that tests the problem-solving skills employed by real engineering.

Microelectronic Circuits by Sedra and Smith has served generations of electrical and computer engineering students as the best and most widely-used text for this required course. Respected equally as a textbook and reference, "Sedra/Smith" combines a thorough presentation of fundamentals with an introduction to present-day IC technology. It remains the best text for helping students progress from circuit analysis to circuit design, developing design skills and insights that are essential to successful practice in the field. Significantly revised with the input of two new coauthors, slimmed down, and updated with the latest innovations, Microelectronic Circuits, Eighth Edition, remains the gold standard in providing the most comprehensive, flexible, accurate, and design-oriented treatment of electronic circuits available today.

Microelectronic Circuit Designis known for being a technically excellent text. The new edition has been revised to make the material more motivating and accessible to students while

retaining a student-friendly approach.Jaeger has added more pedagogy and an emphaisis on design through the use of design examples and design notes. Some pedagogical elements include chapter opening vignettes, chapter objectives, "Electronics in Action" boxes, a problem solving methodology, and "design note" boxes. The number of examples, including new design examples, has been increased, giving students more opportunity to see problems worked out. Additionally,some of the less fundamental mathematical material has been moved to the ARIS website. In addition this edition comes with aHomework Management System called ARIS, which includes 450 static problems.

This market-leading textbook continues its standard of excellence and innovation built on the solid pedagogical foundation of previous editions. This new edition has been thoroughly updated to reflect changes in technology, and includes new BJT/MOSFET coverage that combines and emphasizes theunity of the basic principles while allowing for separate treatment of the two device types where needed. Amply illustrated by a wealth of examples and complemented by an expanded number of well-designed end-of-chapter problems and practice exercises, Microelectronic Circuits is the most currentresource available for teaching tomorrow's engineers how to analyze and design electronic circuits.

This book addresses the need for energy-efficient amplifiers, providing gain enhancement strategies, suitable to run in

parallel with lower supply voltages, by introducing a new family of single-stage cascode-free amplifiers, with proper design, optimization, fabrication and experimental evaluation. The authors describe several topologies, using the UMC 130 nm CMOS technology node with standard-VT devices, for proof-of-concept, achieving results far beyond what is achievable with a classic single-stage folded-cascode amplifier. Readers will learn about a new family of circuits with a broad range of applications, together with the familiarization with a state-of-the-art electronic design automation methodology used to explore the design space of the proposed circuit family.

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